Docket No.

240262US2S/hc

# PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Kenichi IMAMIYA, et al.

SERIAL NO:

10/617,400

GAU:

2818

FILED:

July 11, 2003

**EXAMINER:** 

FOR:

NONVOLATILE SEMICONDUCTOR MEMORY AND ITS TEST METHOD

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

Applicant(s) wish to disclose the following information.

### REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

#### RELATED CASES

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

#### CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of
- □ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

#### DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Marvin J. Spivak

Registration No. 24,913

Joseph A. Scafetta, Jr.

Registration No. 26, 803

Customer Number

22850

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03)

#### IN THE UNITED STATES PATE AND TRADEMARK OFFICE

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## **STATEMENT OF RELEVANCY**

All of the references on Form PTO 1449 are discussed in the specification.

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				U.S. PATENT DOCUMENTS			
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		OTHER RE	FERENCES (	Including Author, Title, Date, Pertinen	t Pages, e	tc.)	
K. IMAMIYA, et al., IEEE Journal of Solid-State Circuits, vol. 34, no. 11, pages 1536-1543, "A 130-mm², 256-Mbit NAND							
		K. IMAMIYA, et al., IEE	E Journal of S	solia-State Circuits, vol. 34, no. 11, bage	S 1536-154		
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